

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S2	50	design\$4 with files same ((hardware adj description adj language) (hdl)) same (path index)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 11:26
S3	4	(generat\$4 construct\$4 creat\$4 build\$4) with ((hardware adj description adj language) (hdl)) same pars\$4 with (description file) same path	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 11:28
S4	179	(generat\$4 construct\$4 creat\$4 build\$4) with ((hardware adj description adj language) (hdl)) same pars\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 11:46
S5	3	(generat\$4 construct\$4 creat\$4 build\$4) with ((hardware adj description adj language) (hdl)) same pars\$4 same index	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 11:29
S6	1	(generat\$4 construct\$4 creat\$4 build\$4) with ((hardware adj description adj language) (hdl)) same pars\$4 same directory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 11:29
S8	43	correlat\$4 near3 description near4 (file language)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 11:52
S9	36	directory with description with file and ((hdl) (hardware adj description adj language))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 11:55
S11	136	index and path and correlat\$4 and ((hdl) (hardware adj description adj language) (verilog)) and directory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 11:57

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S12	118	index and path and correlat\$4 and ((hdl) (hardware adj description adj language) (verilog)) and directory and pars\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 12:06
S13	1	index and path and ((hdl) (hardware adj description adj language) (verilog)) and (design adj file adj name) and pars\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 12:07
S15	4	((hdl) (hardware adj description adj language) (verilog)) and (design adj file adj name) and ic	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 12:09
S16	34	((hdl) (hardware adj description adj language) (verilog)) and (file adj path)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 12:09
S17	19	((hdl) (hardware adj description adj language) (verilog)) and (file adj path) and pars\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 12:13
S19	16	((hdl) (hardware adj description adj language) (verilog)) same ((RTL) (register adj transfer adj language)) and net\$list and (list adj file)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 12:17
S22	12	((hdl) (hardware adj description adj language) (verilog)) and ((IC) (integrated adj circuit) with design with file) and ((RTL) (register adj transfer adj language)) and (directory adj tree)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 12:23
S23	5	(manag\$4) with ((IC) (integrated adj circuit) with design with file) same ((HDL) (hardware adj description adj language))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 12:28

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S25	217	((IC) (integrated adj circuit) with design with file) and ((HDL) (hardware adj description adj language)) and (net\$list) and pars\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 12:30
S26	127	((IC) (integrated adj circuit) with design with file) and ((HDL) (hardware adj description adj language)) and (net\$list) and pars\$4 and ((RTL) (register adj transfer adj language))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 12:54
S27	149	(pars\$4 analyz44) with (description) same ((hdl) (vhdl) (hardware adj description adj language))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 12:55